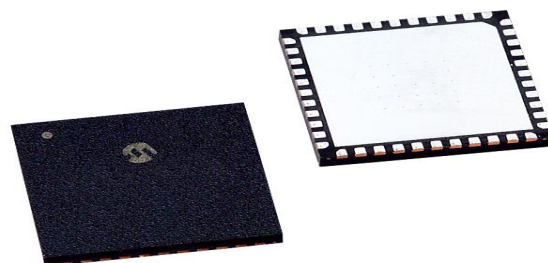




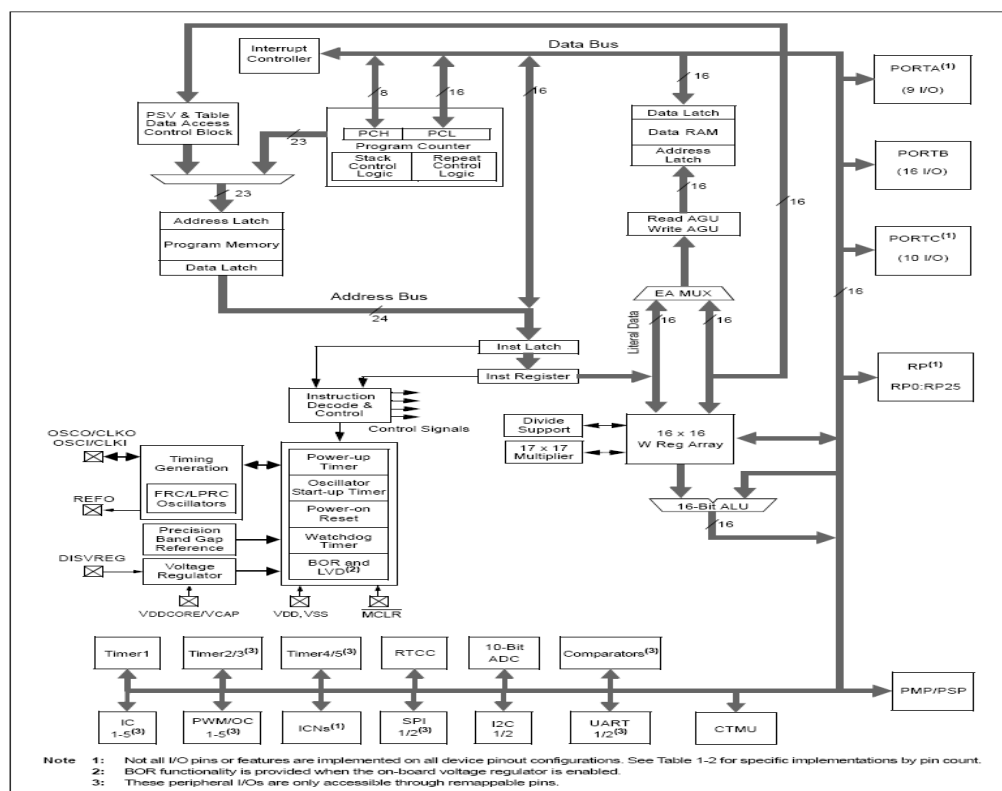
PIC24FJ64GA102 General Purpose Flash Microcontrollers with nanoWatt XLP Technology

General Description:

The PIC24F architecture specification was created as a powerful extension of Microchip's existing RISC microcontroller portfolio. By giving users an even greater range of options for computational power and rich peripheral sets, it allows users to grow their applications. At the same time, the architecture was tailored to keep as much of the existing PICmicro® MCU feature set and nomenclature as possible, making it easy for applications to make the jump.



The PIC24FJ64GA104 family provides an expanded peripheral feature set and a new option for high-performance applications which may need more than an 8-bit platform, but do not require the power of a digital signal processor.



Key Features:

CPU Features

- Up to 16 MIPS performance
- 16 x 16 Hardware Multiply, Single Cycle Execution
- 32-bit x 16-bit Hardware Divider
- C Compiler Optimized Instruction Set System
- Internal oscillator support - 31 kHz to 8 MHz, up to 32 MHz with 4X PLL.
- On-chip LDO Voltage Regulator
- JTAG Boundary Scan and Flash Memory Program Support
- Fail-Safe Clock Monitor – allows safe shutdown if clock fails
- Watchdog Timer with separate RC oscillator nanoWatt Power Managed Modes
- Run, Idle and Sleep modes
- Deep sleep mode for lowest current consumption
- Multiple, Switchable Clock Modes for Optimum Performance and Power Management.

Analog Features

- 10-bit ADC, 9 channels, 500k samples per second
- 3 analog comparators Other Peripherals
- 2 UART Modules with LIN and IrDA® support, 4 Deep FIFO
- 2 SPI Modules with 8 Deep FIFO
- 2 I2C™ Modules with Master and Slave Modes
- Five 16-bit Timer Modules
- Up to 5 Input Capture and 5 Output Compare / PWM, all with dedicated timers
- Hardware RTCC, Real-Time Clock Calendar with Alarms
- PMP, Parallel Master Port, with 16 Address Lines, and 8/16-bit Data
- Peripheral Pin Select for remapping digital peripherals to I/O
- Charge Time Measurement Unit (CTMU) for capacitive touch interface.

Universal Serial Bus Features

- USB v2.0 On-the-Go compliant
- Dual role capable, can act as either Host or Device
- Low Speed(1.5Mb/s) and Full Speed(12 Mb/s) operation in Host mode
- Full speed USB operaton in Device mode
- Supports 32 endpoints
- On-chip USB transceiver

Applications:

- Medical Solutions

- Human Interface
- Motor Control
- Intelligent Power Supply
- Lighting
- Wired & Wireless Connectivity
- Mechatronics
- Automotive Solutions
- Low power remote transmitters/receivers

Related Products Information:

Mfr Part #	Farnell #	Newark #	Description
PIC24FJ64GA102-I/ML	1778477	07P9732	MCU, 16BIT, 64K FLASH, 16MIPS, 28QFN
PIC24FJ64GA102-I/SO	1778478	07P9733	MCU, 16BIT, 64K FLASH, 16MIPS, 28SOIC
PIC24FJ64GA102-I/SP	1778479	07P9734	MCU, 16BIT, 64KFLASH, 16MIPS, 28SPDIP
PIC24FJ64GA104-I/ML	1778480	45P4822	MCU, 16BIT, 64K FLASH, 16MIPS, 44QFN
PIC24FJ64GA104-I/PT	1778482	45P4823	MCU, 16BIT, 64K FLASH, 16MIPS, 44TQFP