

Fig.1 Recommended PCB Layout (All dimensions are symmetric about the center line)

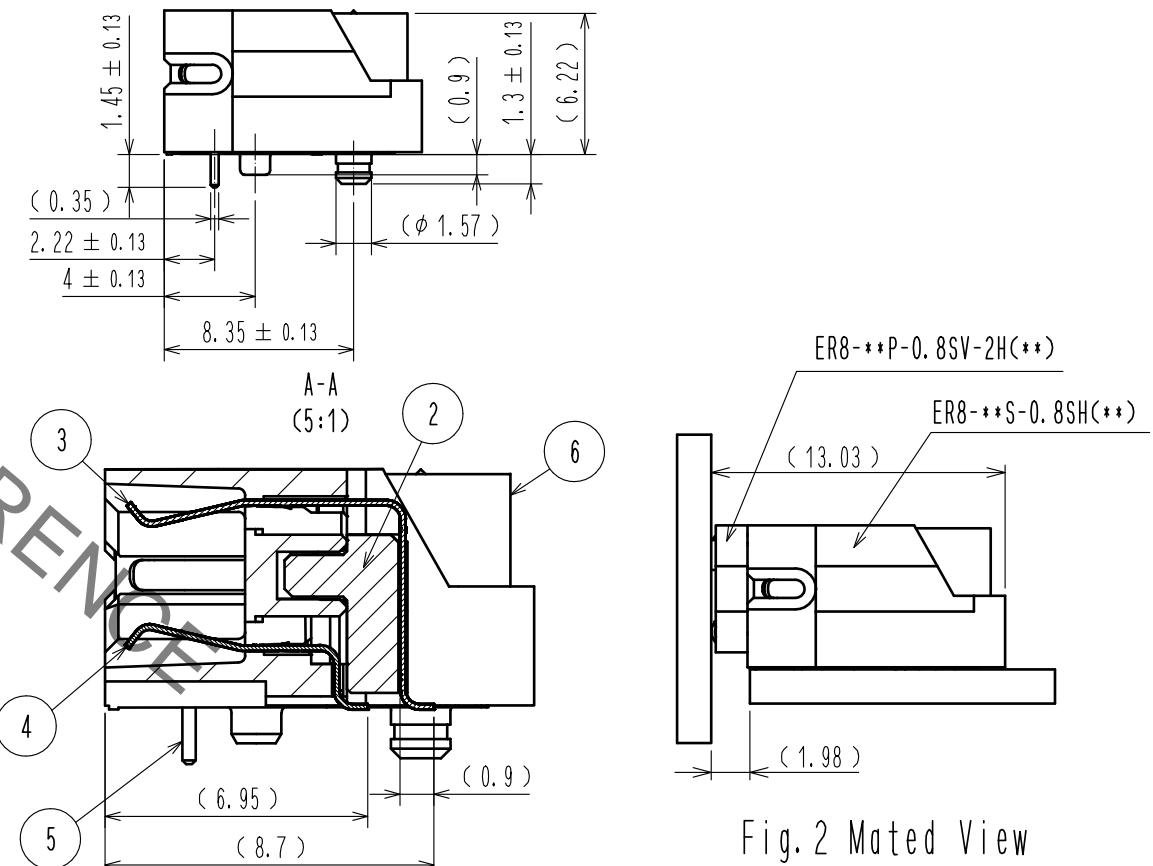
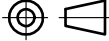






Fig. 2 Mated View

- NOTES
1. Lead co-planarity of this product to be 0.15MAX.
  2. ( ) indicates a reference dimension.
  3. Packaging for this product applies embossed carrier tape packaging. 200pcs per reel. See sheet 2/2 for details.
  4. Lot No. shall be marked on the indicated position.

3	COPPER ALLOY	CONTACT AREA : Ni1.25 $\mu$ m + Au0.25 $\mu$ m		6	COPPER ALLOY	Ni1.25 $\mu$ m + Sn2.54 $\mu$ m				
		LEAD AREA : Ni1.25 $\mu$ m + Sn2.54 $\mu$ m		5	COPPER ALLOY	Ni1.25 $\mu$ m + Sn2.54 $\mu$ m				
2	LCP	BLACK UL94V-0		4	COPPER ALLOY	CONTACT AREA : Ni1.25 $\mu$ m + Au0.25 $\mu$ m				
1	LCP	BLACK UL94V-0				LEAD AREA : Ni1.25 $\mu$ m + Sn2.54 $\mu$ m				
NO.	MATERIAL	FINISH . REMARKS			NO.	MATERIAL	FINISH . REMARKS			
UNITS mm				SCALE 3 : 1		COUNT	DESCRIPTION OF REVISIONS	DESIGNED	CHECKED	DATE
 HIROSE ELECTRIC CO., LTD.		APPROVED : MK. EZAKI		17. 03. 31		DRAWING NO.		EDC-359657-10-00		
		CHECKED : MK. EZAKI		17. 03. 31		PART NO.		ER8-120S-0. 8SH<10>		
		DESIGNED : TF. SUGAWARA		17. 03. 30		CODE NO.		CL625-1001-4-10		
		DRAWN : TF. SUGAWARA		17. 03. 30				 		

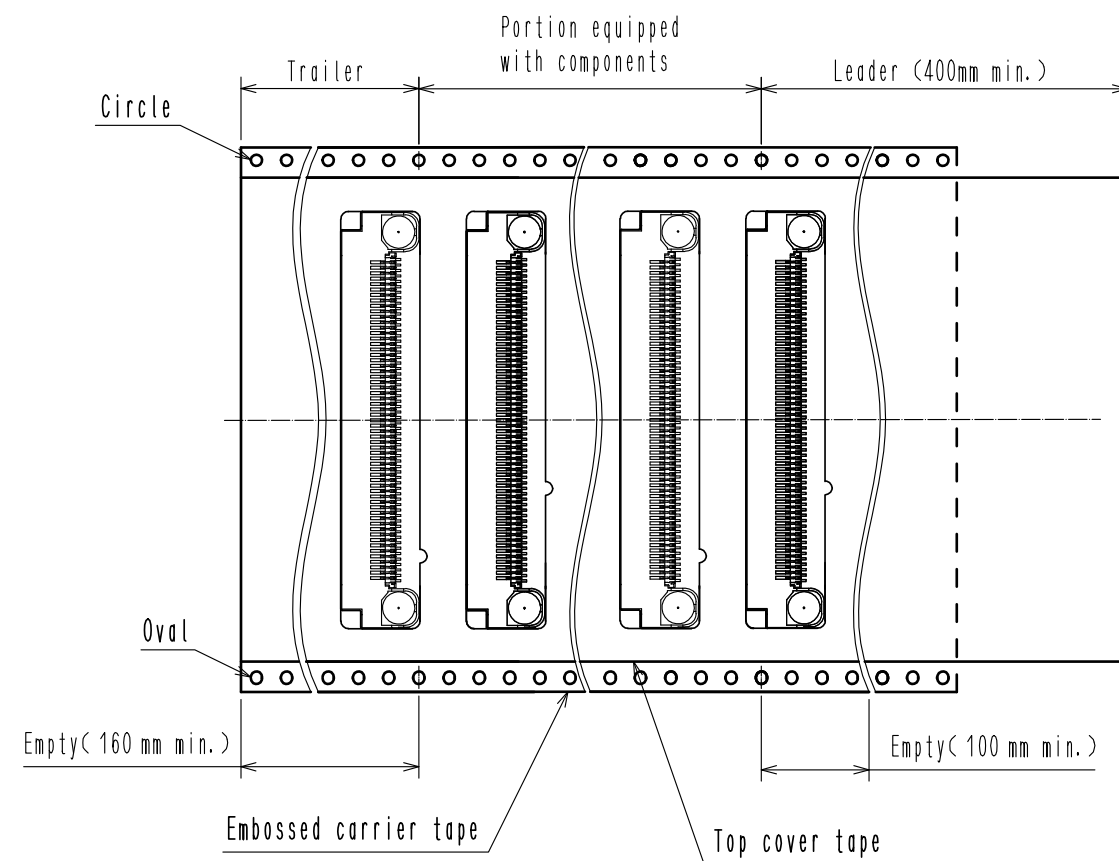
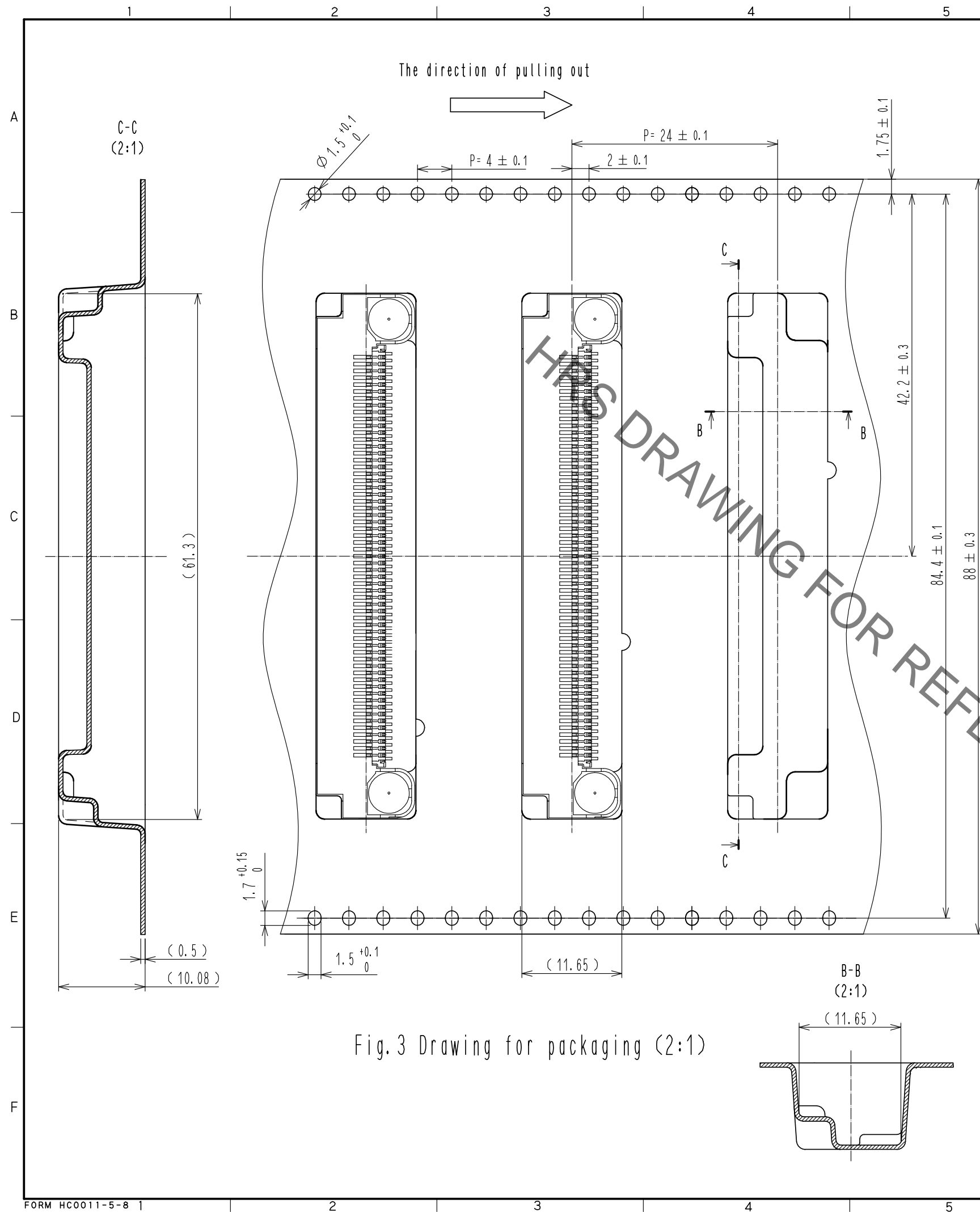


Fig.4 (Free)

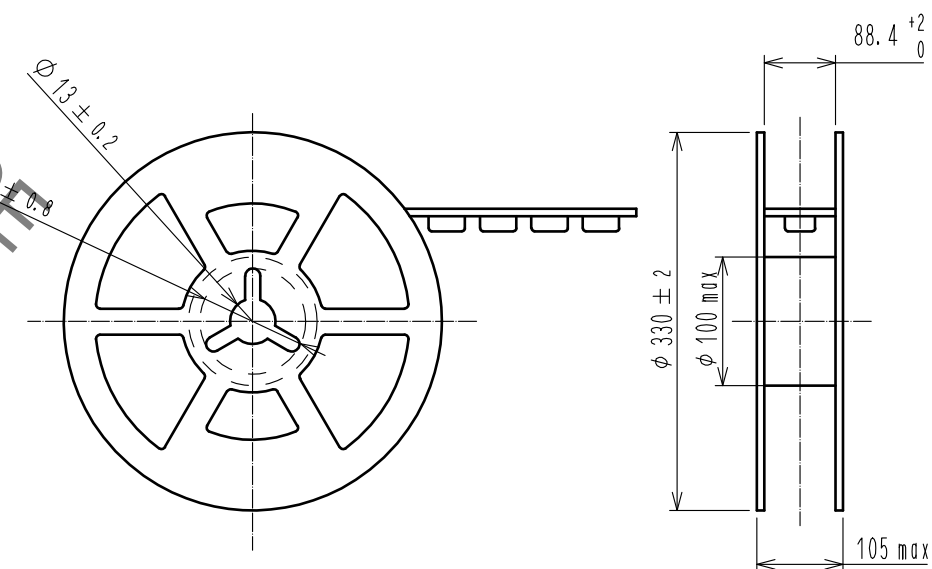


Fig.5 Detail of reer (Free)

**HRS**

DRAWING NO.	EDC-359657-10-00
PART NO.	ER8-120S-0.8SH(10)
CODE NO.	CL625-1001-4-10

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