



PRODUCT BULLETIN

Generic Copy

ISSUE DATE: 17-Apr-2013
NOTIFICATION: 15630
TITLE: KINETIS K-series 120/150 MHz products - ERRATA UPDATE
EFFECTIVE DATE: 18-Apr-2013

DEVICE(S)

MPN
MK10FN1M0VLQ12
MK10FN1M0VMD12
MK10FX512VLQ12
MK10FX512VMD12
MK20FN1M0VLQ12
MK20FN1M0VMD12
MK20FX512VLQ12
MK20FX512VMD12
MK60FN1M0VLQ12
MK60FN1M0VLQ15
MK60FN1M0VMD12
MK60FN1M0VMD12R
MK60FN1M0VMD15
MK60FX512VLQ12
MK60FX512VLQ15
MK60FX512VMD12
MK60FX512VMD15
MK61FN1M0VMD12
MK61FN1M0VMD15
MK61FN1M0VMJ12
MK61FN1M0VMJ15
MK61FX512VMD12
MK61FX512VMD15
MK61FX512VMJ12
MK61FX512VMJ15
MK70FN1M0VMJ12
MK70FN1M0VMJ15
MK70FX512VMJ12

AFFECTED CHANGE CATEGORIES

- DEVICE SPECIFICATION / ERRATA

DESCRIPTION OF CHANGE

The Kinetis 3N96B mask set errata documentation has been updated to include additional errata that have been identified.

The following errata have been added:

6378 Cache: Cache write buffer error enable (MCM_ISCR[CWBEE]) does not work

6358 ENET: Write to Transmit Descriptor Active Register (ENET_TDAR) is ignored

4710 FTM: FTM2 and FTM3 do not correctly enter BDM mode when a debugger connection is active

6573 JTAG: JTAG TDO function on the PTA2 disables the pull resistor

4546 TSI: The counter values reported from TSI increase when in low power modes (LLS, VLLS1, VLLS2, VLLS3)

6472 UART: ETU compensation needed for ISO-7816 wait time (WT) and block wait time (BWT)

5704 UART: TC bit in UARTx_S1 register is set before the last character is sent out in ISO7816 T=0 mode

The Kinetis 3N96B mask set errata documentation has been updated to remove errata that have been determined to not affect the device.

The following errata have been removed:

2550 ADC: ADC abort conversion logic error

4378 JTAGM: Internal TCK may be disabled on start up preventing use of the internal software debug feature

The updated errata documentation is attached to this notice and can be found at http://cache.freescale.com/files/microcontrollers/doc/errata/KINETIS_3N96B.pdf

REASON FOR CHANGE

The errata document for the Kinetis 3N96B mask set has been updated.

ANTICIPATED IMPACT OF PRODUCT CHANGE(FORM, FIT, FUNCTION, OR RELIABILITY)

No changes have been made to the current production device. The errata describe existing conditions identified on current production devices. There are potential hardware and/or software implications to customers.

NOTE:

THE CHANGE(S) SPECIFIED IN THIS NOTIFICATION WILL BE IMPLEMENTED ON THE EFFECTIVE DATE LISTED ABOVE. To request further data or inquire about the notification, please enter a [Service Request](#)

For sample inquiries - please go to www.freescale.com

QUAL DATA AVAILABILITY DATE: 09-Apr-2013

QUALIFICATION STATUS: COMPLETED

QUALIFICATION PLAN:

N/A

RELIABILITY DATA SUMMARY:

N/A

ELECTRICAL CHARACTERISTIC SUMMARY:

N/A

CHANGED PART IDENTIFICATION:

N/A

ATTACHMENT(S):

External attachment(s) FOR this notification can be viewed AT:

[15630_KINETIS_3N96B_03262013.pdf](#)
