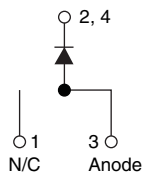


# Ultralow $V_F$ Ultrafast Rectifier, 6 A FRED Pt®



TO-252AA (D-PAK)



## FEATURES

- Ultrafast recovery time, extremely low  $V_F$  and soft recovery
- 175 °C maximum operating junction temperature
- For PFC DCM operation
- Low leakage current
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- Material categorization: for definitions of compliance please see [www.vishay.com/doc?99912](http://www.vishay.com/doc?99912)



**RoHS**  
COMPLIANT  
**HALOGEN**  
**FREE**

## DESCRIPTION / APPLICATIONS

State of the art hyperfast recovery rectifiers designed with optimized performance of forward voltage drop, hyperfast recovery time, and soft recovery.

The planar structure and the platinum doped life time control guarantee the best overall performance, ruggedness and reliability characteristics.

These devices are intended for use in PFC boost stage in the AC/DC section of SMPS inverters or as freewheeling diodes. Their extremely optimized stored charge and low recovery current minimize the switching losses and reduce over dissipation in the switching element and snubbers.

## PRODUCT SUMMARY

Package	TO-252AA (D-PAK)
$I_{F(AV)}$	6 A
$V_R$	600 V
$V_F$ at $I_F$	0.87 V
$t_{rr}$ (typ.)	59 ns
$T_J$ max.	175 °C
Diode variation	Single die

## ABSOLUTE MAXIMUM RATINGS

PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Peak repetitive reverse voltage	$V_{RRM}$		600	V
Average rectified forward current	$I_{F(AV)}$	$T_C = 156\text{ °C}$	6	A
Non-repetitive peak surge current	$I_{FSM}$	$T_J = 25\text{ °C}$	80	
Peak repetitive forward current	$I_{FM}$	$T_C = 156\text{ °C}$ , $f = 20\text{ kHz}$ , $d = 50\%$	12	
Operating junction and storage temperatures	$T_J, T_{Stg}$		-65 to +175	°C

## ELECTRICAL SPECIFICATIONS ( $T_J = 25\text{ °C}$ unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Breakdown voltage, blocking voltage	$V_{BR}, V_R$	$I_R = 100\text{ }\mu\text{A}$	600	-	-	V
Forward voltage	$V_F$	$I_F = 6\text{ A}$	-	0.99	1.25	
		$I_F = 6\text{ A}$ , $T_J = 150\text{ °C}$	-	0.87	1.05	
Reverse leakage current	$I_R$	$V_R = V_R$ rated	-	-	5	$\mu\text{A}$
		$T_J = 150\text{ °C}$ , $V_R = V_R$ rated	-	-	125	
Junction capacitance	$C_T$	$V_R = 600\text{ V}$	-	3.5	-	pF
Series inductance	$L_S$	Measured lead to lead 5 mm from package body	-	8	-	nH



DYNAMIC RECOVERY CHARACTERISTICS ( $T_J = 25\text{ }^{\circ}\text{C}$ unless otherwise specified)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Reverse recovery time	$t_{rr}$	$I_F = 1\text{ A}$ , $dI_F/dt = 100\text{ A}/\mu\text{s}$ , $V_R = 30\text{ V}$	-	59	70	ns
		$I_F = 1\text{ A}$ , $dI_F/dt = 50\text{ A}/\mu\text{s}$ , $V_R = 30\text{ V}$	-	75	-	
		$T_J = 25\text{ }^{\circ}\text{C}$	-	154	-	
		$T_J = 125\text{ }^{\circ}\text{C}$	-	215	-	
Peak recovery current	$I_{RRM}$	$T_J = 25\text{ }^{\circ}\text{C}$	-	13.3	-	A
		$T_J = 125\text{ }^{\circ}\text{C}$	-	15.4	-	
Reverse recovery charge	$Q_{rr}$	$T_J = 25\text{ }^{\circ}\text{C}$	-	1055	-	nC
		$T_J = 125\text{ }^{\circ}\text{C}$	-	1600	-	

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Maximum junction and storage temperature range	$T_J, T_{Stg}$		-65	-	175	$^{\circ}\text{C}$
Thermal resistance, junction to case per leg	$R_{thJC}$		-	-	3	$^{\circ}\text{C}/\text{W}$
Approximate weight			0.3			g
			0.01			oz.
Marking device		Case style TO-252AA (D-PAK)	6EWL06FN			

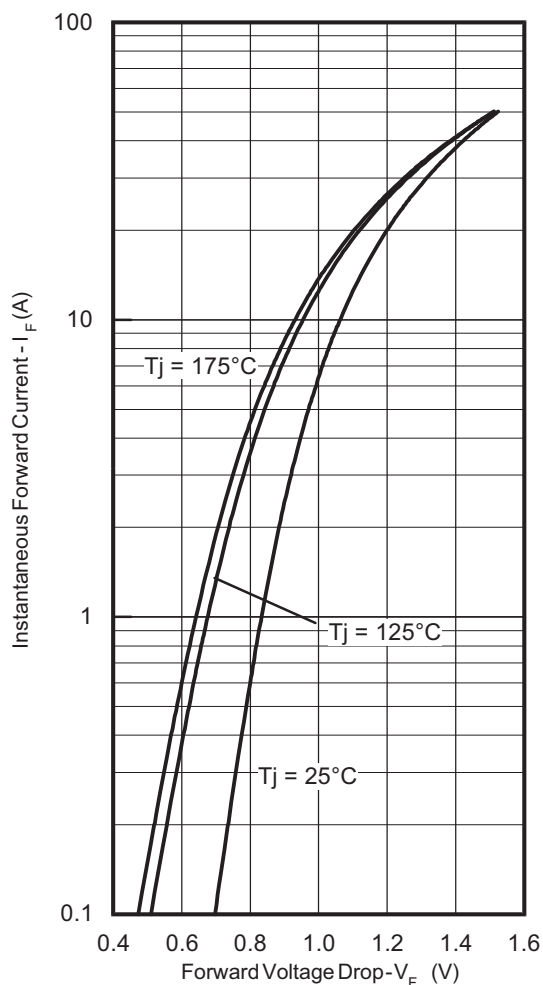


Fig. 1 - Typical Forward Voltage Drop Characteristics

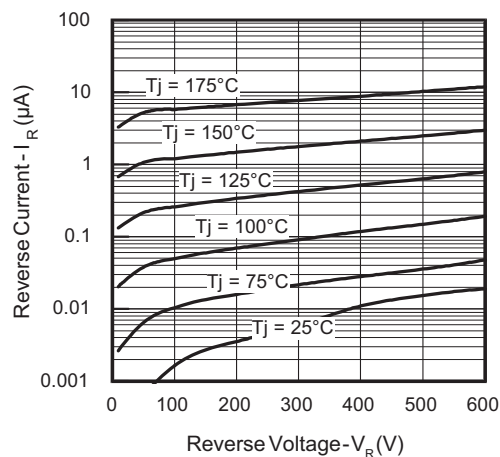


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

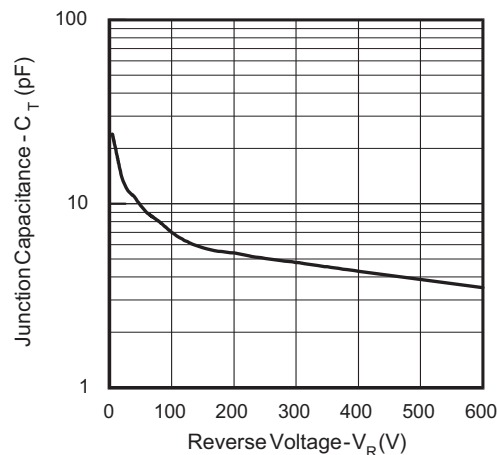
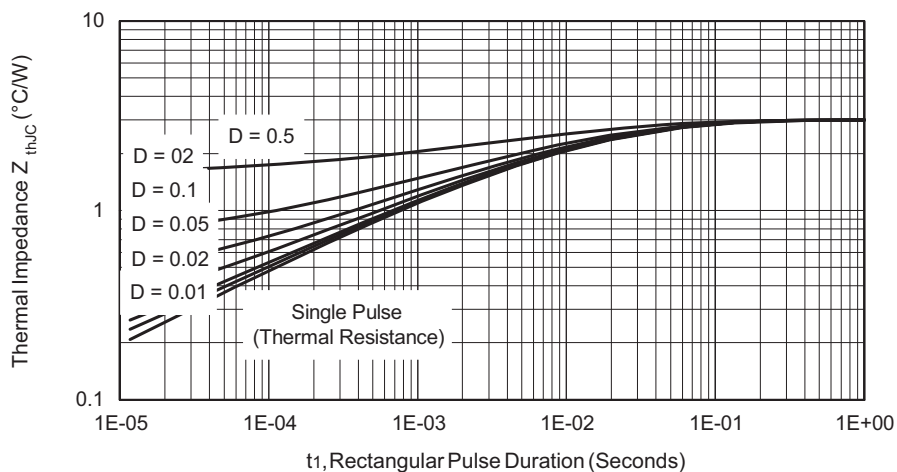


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage


Fig. 4 - Maximum Thermal Impedance  $Z_{thJC}$  Characteristics

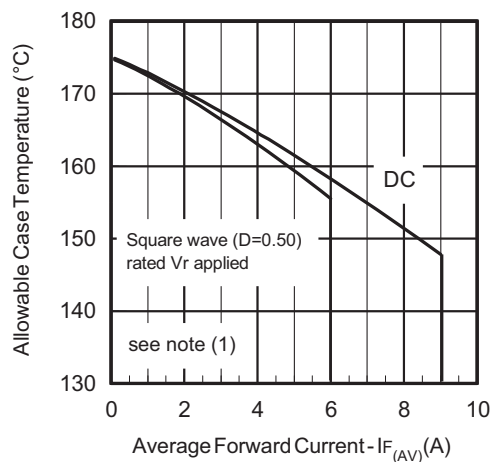


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current

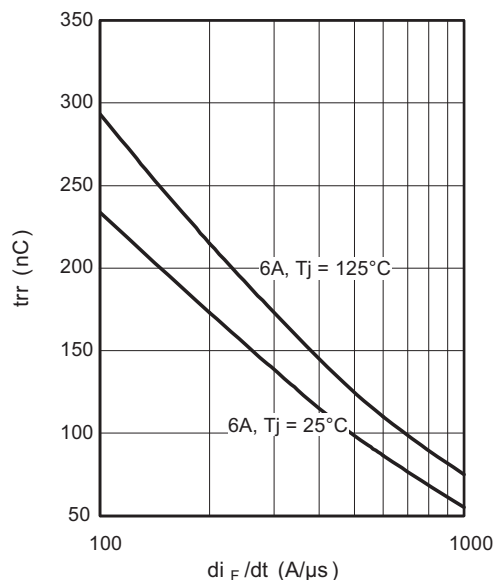


Fig. 7 - Typical Reverse Recovery Time vs.  $dI_F/dt$

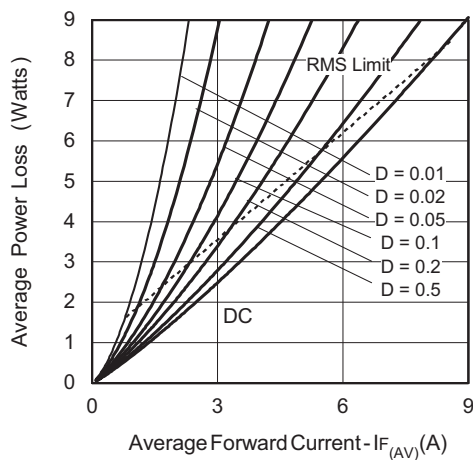


Fig. 6 - Forward Power Loss Characteristics

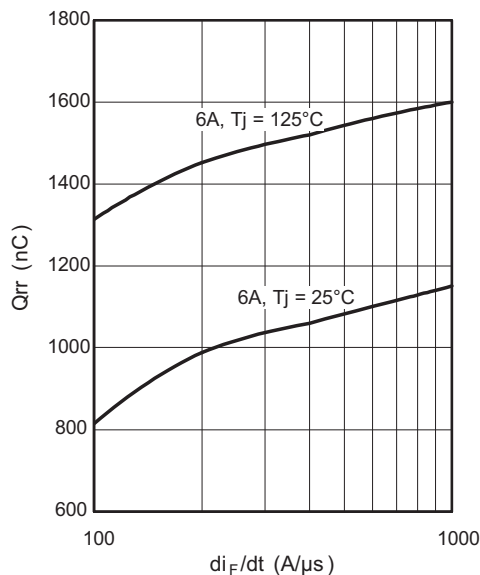


Fig. 8 - Typical Stored Charge vs.  $dI_F/dt$

#### Note

- (1) Formula used:  $T_C = T_J - (P_d + P_{dREV}) \times R_{thJC}$ ;  
 $P_d$  = Forward power loss =  $I_{F(AV)} \times V_{FM}$  at  $(I_{F(AV)}/D)$  (see fig. 6);  
 $P_{dREV}$  = Inverse power loss =  $V_{R1} \times I_R (1 - D)$ ;  $I_R$  at  $V_{R1}$  = Rated  $V_R$

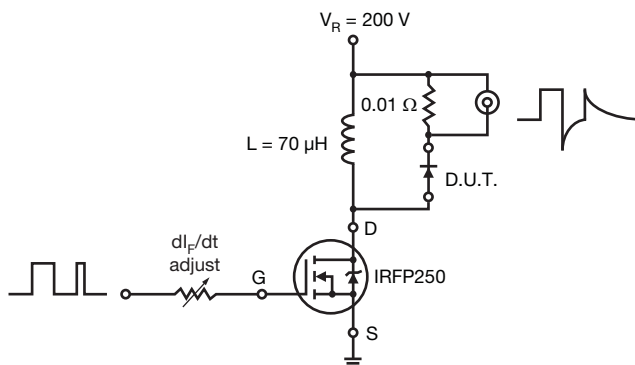


Fig. 9 - Reverse Recovery Parameter Test Circuit

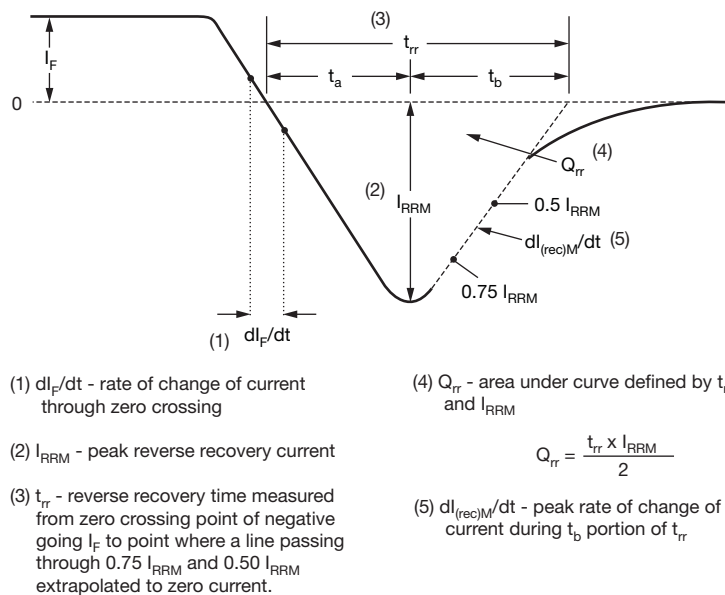


Fig. 10 - Reverse Recovery Waveform and Definitions

**ORDERING INFORMATION TABLE**

Device code	VS-	6	E	W	L	06	FN	TRL	-M3
	1	2	3	4	5	6	7	8	9

- |          |   |   |
|----------|---|---|
| <b>1</b> | - | Vishay Semiconductors product   |
| <b>2</b> | - | Current rating (6 = 6 A)  |
| <b>3</b> | - | Circuit configuration:<br>E = single diode  |
| <b>4</b> | - | Package identifier:<br>W = D-PAK  |
| <b>5</b> | - | L = low $V_F$ , fast recovery   |
| <b>6</b> | - | Voltage rating (06 = 600 V)   |
| <b>7</b> | - | FN = TO-252AA   |
| <b>8</b> | - | <ul style="list-style-type: none"><li>• None = tube</li><li>• TR = tape and reel</li><li>• TRL = tape and reel (left oriented)</li><li>• TRR = tape and reel (right oriented)</li></ul> |
| <b>9</b> | - | Environmental digit:<br>-M3 = halogen-free, RoHS-compliant and terminations lead (Pb)-free  |

<b>ORDERING INFORMATION</b> (Example)			
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION
VS-6EWL06FN-M3	75	3000	Antistatic plastic tube
VS-6EWL06FNTR-M3	2000	2000	13" diameter reel
VS-6EWL06FNTRL-M3	3000	3000	13" diameter reel
VS-6EWL06FNTRR-M3	3000	3000	13" diameter reel

<b>LINKS TO RELATED DOCUMENTS</b>	
Dimensions	<a href="http://www.vishay.com/doc?95016">www.vishay.com/doc?95016</a>
Part marking information	<a href="http://www.vishay.com/doc?95176">www.vishay.com/doc?95176</a>
SPICE model	<a href="http://www.vishay.com/doc?95218">www.vishay.com/doc?95218</a>





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