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PRODUCT INFORMATION NOTIFICATION

PIN: PIN173101

Date: August 07, 2017

Subject: Qualification of ChipMOS Shanghai as an Additional Wafer Sort Site for Select NOR Products

To: GENERAL INBOX
PREMIER FARNELL
ProductChangeNotices@premierfarnell.com

Change Type: Minor

Description of Change:

Cypress announces the qualification of ChipMOS Technologies Ltd in Shanghai as an additional Wafer Sort Site for select NOR products. This wafer sort site has been in operation for 15 years and is an ISO9001 and TS16959 qualified site.

The ChipMOS Technologies Ltd (Shanghai) site is in the Qingpu Industrial Zone of Shanghai, China. The test area has over 30,000 sq. feet of class 1000 clean room and supports over 2000 sort outs each week. The facility is a provider of semiconductor manufacturing services in assembly, test, and finish processes. Their assembly and test facilities in Taiwan are already a qualified site for Cypress products. This qualification will enable these products to have a second source test site, and leverage ChipMOS' manufacturing expertise and quality focus. This in turn provides the means for Cypress to continue to meet our customers' requirements and meet our delivery commitments in dynamic market conditions.

Hardware and test programs are the same as at the currently qualified sort sites.

Benefit of Change:

This change will provide a second wafer sort site for select NOR products. This will expand the sort test capacity of Cypress to support the delivery of these products to Cypress' customers. ChipMOS has been a longtime partner for Flash sort. The ChipMOS, Taiwan facility has been a fully qualified sort location for Flash sort. The tool-set used for Flash sort, in ChipMOS Shanghai was transferred from ChipMOS Taiwan. The executive management team in ChipMOS Taiwan will oversee the sort facility in ChipMOS Shanghai. The products that will be supported by ChipMOS Shanghai are non-automotive Flash products.

Cypress will have the added capability to meet varying market demand, and to ensure consistent and reliable delivery performance to customers.

Part Numbers Affected: 93

See the attached 'Affected Parts List' file for a list of all part numbers affected by this change. Note that any new parts that are introduced after the publication of this PIN will include all changes outlined in this PIN.

Qualification Status:

This Wafer Sort site has been qualified through a series of tests documented in the Qualification Test Plan QTP172704. This qualification report can be found as an attachment to this PIN.

Approximate Implementation Date:

This change will be implemented effective with the date of this notification.

Anticipated Impact:

Products sorted at ChipMOS Shanghai are completely compatible with existing products from a functional, parametric, and quality performance perspective. Products fabricated at the new site are completely compatible with existing products from a functional, parametric, and quality performance perspective.

Cypress also recommends that customers take this opportunity to review these changes against current application notes, system design considerations and customer environment conditions to assess impact (if any) to their application.

Method of Identification:

Cypress maintains traceability of product to wafer level, including wafer fabrication location, through the lot number marked on the package.

Response Required:

No response is required.

For additional information regarding this change, contact your local sales representative or contact the PCN Administrator at pcn_adm@cypress.com.

Sincerely,

Cypress PCN Administration