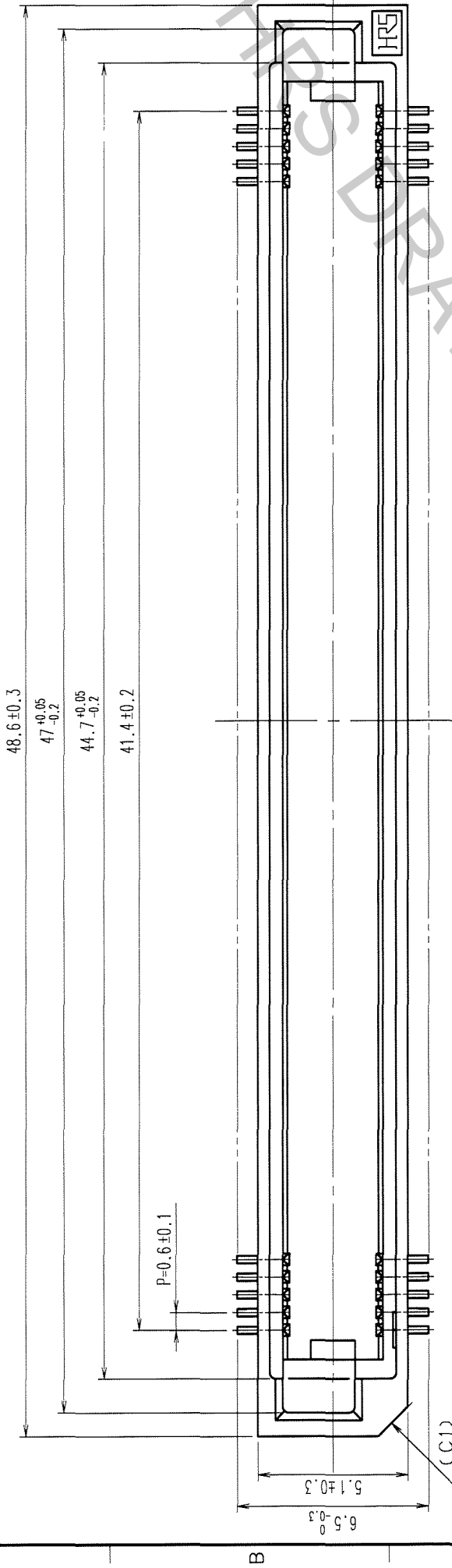


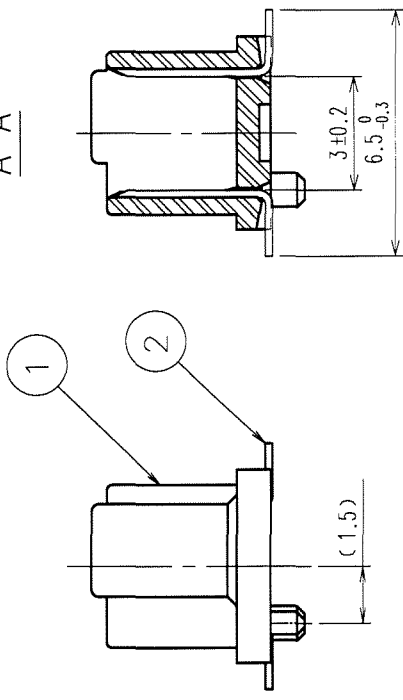
In case that the application demands a high level of reliability, such as automotive, please contact a company representative for further information.

| COUNT DESCRIPTION OF REVISIONS | | DATE | COUNT | DESCRIPTION OF REVISIONS | BY | CHKD | DATE |
|--------------------------------|--|------|-------|--------------------------|----|------|------|
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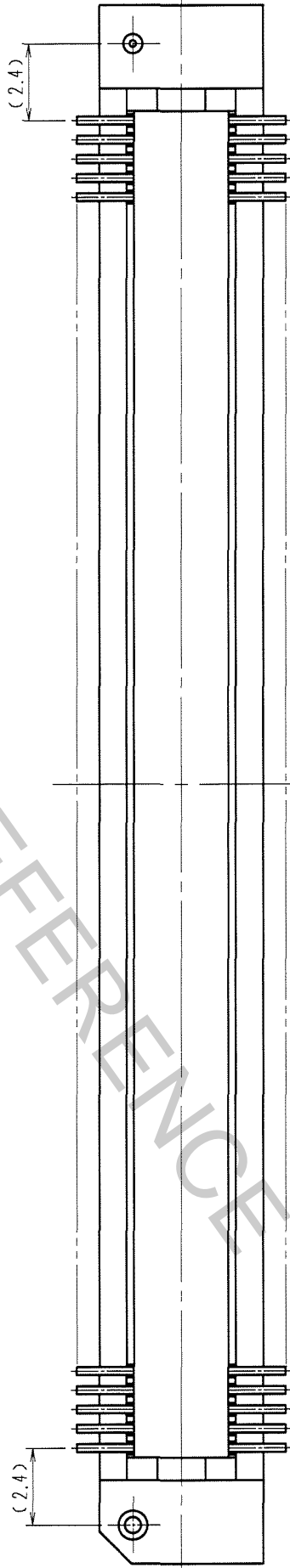


A-A



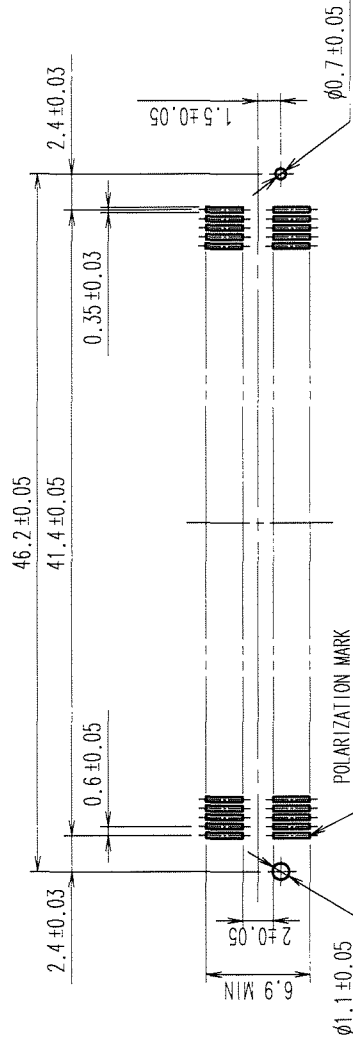
10.2±0.03×w0.25±0.03
(φ0.6)

46.2±0.1
(φ0.9)



- NOTE 1. LEAD CO-PLANARITY SHALL BE 0.1mm MAX.
2. FOR MODIFICATION, ETC., PREVENTIVE HOLE FOR SINK MARK MAY BE ADDED ACCORDING TO CIRCUMSTANCES.
3. THE DIMENSIONS IN PARENTHESES ARE FOR REFERENCE.

RECOMMENDED LAND PATTERN DIMENSION OF PCB (MOUNTING SIDE) (2:1)



| | | | | | | | | |
|---------------------------|----------|-----------------------|-------------------------|---------------------------------|--|----------|--|--|
| 1 | PPS | LIGHT BROWN (UL94V-0) | 2 | PHOSPHOR BRONZE | CONTACT AREA: GOLD 0.1μm min. LEAD AREA: GOLD FLASH UNDER PLATING: NICKEL 0.5μm min. | | | |
| NO. | MATERIAL | FINISH, REMARKS | NO. | MATERIAL | FINISH, REMARKS | | | |
| CODE NO. (OLD) | | DRAWN K. NAKAMURA | DESIGNED K. NAKAMURA | CHECKED K. Ogawa | APPROVED H. Okawa | RELEASED | | |
| CL | | | 05.11.15 | 05.11.15 | '05.11.16 | | | |
| DRAWING NO. | | PART NO. | | F X 8 C - 1 4 0 P - S V (7 1) | | | | |
| EDC3-150825-25 | | | | | | | | |
| SCALE | | | | | | | | |
| 5 : 1 | | | | | | | | |
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| HIROSE ELECTRIC CO., LTD. | | | | | | | | |
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