

Final Product Change Notification

201903006F01

Issue Date: 01-Jul-2019

Effective Date: 13-Oct-2019

Dear *Emma Tempest*,

Here's your personalized quality information concerning products Premier Farnell PLC purchased from Nexperia.

For detailed information we invite you to [view this notification online](#)



Change Category

☒ Wafer Fab Process

☒ Wafer Fab Materials

☐ Wafer Fab Location

☐ Assembly Process

☐ Assembly Materials

☐ Assembly Location

☐ Product Marking

☐ Mechanical Specification

☐ Packing/Shipping/Labeling

☐ Test Location

☐ Test Process

☐ Test Equipment

☐ Design

☐ Errata

☐ Electrical spec./Test coverage

Release of 8 inch wafer diameter for resistor-equipped transistors (RET) in SOT363

Details of this Change

Release of production using 8 inch wafer diameter, 2nd source epitaxy and new doping material for the poly silicon resistors for resistor-equipped transistors (RET) in SOT363 package.

For some affected products the 8 inch conversion is combined with the introduction of a smaller die pitch size.

- (1) Release of production using 8 inch wafer diameter for all product types.
- (2) 2nd source epitaxy supplier for all product types.
- (3) New doping material for the poly silicon resistors for all product types.
- (4) A few product types will be changed to a smaller die pitch size (330 μm x 330 μm instead of 400 μm x 400 μm).

Old Products:

- 6 inch wafer diameter
- inhouse epitaxy

- current doping material for the poly silicon resistors
- 400 µm x 400 µm die pitch size (where affected)

Changed Products:

- 6 inch or 8 inch wafer diameter
- inhouse epitaxy (6 inch and 8 inch) or external epitaxy (8 inch) wafer diameter
- old doping material (6 inch) or new doping material (8 inch) for the poly silicon resistors
- 400 µm x 400 µm (6 inch) or 330 µm x 330 µm (8 inch) die pitch size (where affected)

Production on 8 inch wafer diameter implies the use of the respective 8 inch wafer process technology.

Why do we Implement this Change

- (1) To increase flexibility and volume ramp-up.
- (2) To increase flexibility, volume ramp-up and reduced supply chain risk.
- (3) Improved resistance linearity.
- (4) Volume ramp-up, increase of wafer fab capacity and flexibility.

Identification of Affected Products

The 8 inch products can be identified by a marker on the die surface.

Changed products can be identified by date code after implementation.

Product Availability

Sample Information

Samples are available upon request

Latest sample request date for PCN samples is 31-July-2019.

Production

Planned first shipment 14-Oct-2019

Impact

No impact to the products' functionality anticipated.

Disposition of Old Products

Supply using 6 inch wafer will be continued in parallel to 8 inch wafer production.

Additional information

Affected products and sales history information: see attached file

Self qualification: [view online](#)

Additional documents: [view online](#)



Timing and Logistics

Your acknowledgement of this change, conform JEDEC J-STD-046, is expected till 31-Jul-2019. Lack of acknowledgement of the PCN constitutes acceptance of the change.

Contact and Support

For all inquiries regarding the ePCN tool application or access issues, please [contact Nexperia "Global Quality Support Team"](#).

For all Quality Notification content inquiries, please contact your local Nexperia Sales Support team.

For specific questions on this notice or the products affected please contact our specialist directly:

e-mail address PCN-Bipolar.Discretes@nexperia.com

At Nexperia B.V. we are constantly striving to improve our product and processes to ensure they reach the highest possible Quality Standards.

About Nexperia B.V.

We at Nexperia are the efficiency semiconductor company. We deliver over 90 billion products a year and as such service thousands of global customers, both directly and through our extensive network of channel partners. We are at the heart of billions of electronic devices in the Automotive, Mobile, Industrial, Consumer, Computing, and Communication Infrastructure segments.

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