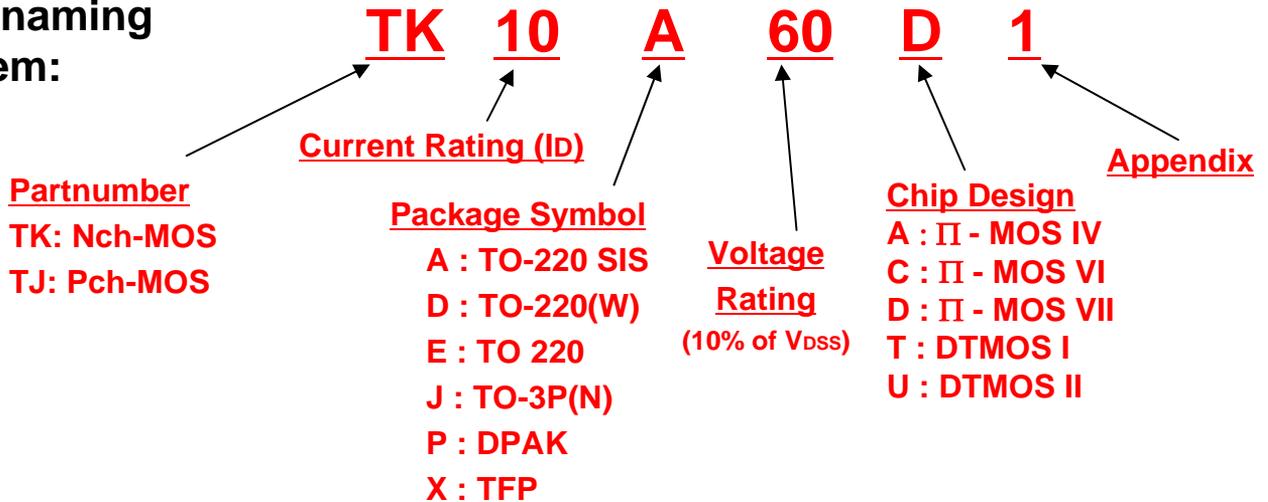


Key product families:

DTMOS : Super Junction style MOSFET -> lower $R_{DS(on)}$ & Q_g
 II-MOS VII: Standard MOSFET -> with low Q_g and capacitance

New naming system:



DTMOS II

> the way to maximize efficiency, whilst minimizing package size...

Characteristics

V_{DSS} : 600V & 650V, I_D =12~50A, $R_{DS(on)}$ =0.065~0.4Ohm
 Packages: pls see opposite page

Features

lowest $Q_g^* R_{DS(on)}$ at market => high efficiency + simple to drive
 low R_{on} by Super Junction style structure
 low gate capacitance
 guaranteed avalanche durability
 high ruggedness concept

Application: SMPS Main Switch & PFC, Lamp Ballast

V_{DS}	Partnumber	Chip Design	I_D	$R_{DS(on)}$ max/ Ohm	Q_g	Packages	Status
600V	TK50J60U	DTMOS II	50A	0.065	70nC	TO-3P(N)	Sample
600V	TK40J60T	DTMOS I	40A	0.08	67nC	TO-3P(N)	MP
600V	TK20J60U	DTMOS II	20A	0.19	27nC	TO-3P(N)	MP
600V	TK20A60U	DTMOS II	20A	0.19	27nC	TO220SIS	MP
600V	TK20D60U	DTMOS II	20A	0.19	27nC	TO220W	MP
600V	TK20E60U	DTMOS II	20A	0.19	27nC	TO220	MP Q2'10
600V	TK20X60U	DTMOS II	20A	0.19	27nC	TFP (SMD)	MP
600V	TK15J60U	DTMOS II	15A	0.3	17nC	TO-3P(N)	MP
600V	TK15A60U	DTMOS II	15A	0.3	17nC	TO220SIS	MP
600V	TK15D60U	DTMOS II	15A	0.3	17nC	TO220W	MP
600V	TK15E60U	DTMOS II	15A	0.3	17nC	TO220	MP Q2'10
600V	TK15X60U	DTMOS II	15A	0.3	17nC	TFP (SMD)	MP
600V	TK12J60U	DTMOS II	12A	0.4	14nC	TO-3P(N)	MP
600V	TK12A60U	DTMOS II	12A	0.4	14nC	TO220SIS	MP
600V	TK12D60U	DTMOS II	12A	0.4	14nC	TO220W	MP
600V	TK12E60U	DTMOS II	12A	0.4	14nC	TO220	MP Q2'10
600V	TK12X60U	DTMOS II	12A	0.4	14nC	TFP (SMD)	MP
650V	TK13A65U	DTMOS II	13A	0.38	17nC	TO220SIS	MP

